

REMARKS

This paper is in response to the official action of June 2, 2005. Reconsideration is requested.

By the foregoing, claims 1 and 5 have been amended for clarity, to particularly point out and distinctly claim the subject matter of the invention. Claims 7-16 have been added, without the addition of new matter.

Figs. 1 and 4 have been amended pursuant to the examiner's requirements. In view thereof, the objection to the drawings should be withdrawn; such action is solicited.

Further, the title of the invention has been amended to read "CMOS Image Sensor" for consistency with the subject matter recited in claims 1-16, as required by the examiner.

The anticipation rejection based on Lee et al. U.S. 6,545,624 is respectfully but strongly traversed. Reconsideration is requested.

Firstly, the Lee et al. patent is not conclusively available as a reference, as the December 30, 2000, Korean priority date for the present application predates the February 8, 2001, U.S. filing date and the June 6, 2002, publication date associated with the reference.

Further, the present invention is clearly distinguishable from the disclosure of Lee et al., at least in that Lee et al do not teach or disclosure a ramp signal, which is adjusted for a desired gamma correction, as recited in the present claims.

It is believed that all claims 1-16 are in condition for allowance, and an indication to that effect is solicited.

Should the examiner wish to discuss the foregoing or any matter of form in an effort to advance this application toward allowance, he is urged to telephone the undersigned at the indicated number.

Respectfully submitted,

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